



Centre of Excellence

VLSI design centre

ChipIN Centre | Chips to Startup (C2S) | C-DAC

(Funded by MeitY)

Vision

- To emerge as a premier Centre of Excellence in VLSI Design and Semiconductor Technology, producing globally competent VLSI engineers.

Mission

- To provide industry-oriented training in VLSI Design, Semiconductor Technology, and EDA tools through hands-on and experiential learning.
- To promote innovation, interdisciplinary research, and chip tapeout in semiconductor technologies through the Chip to Startup (C2S) Programme.
- To develop technically competent, ethically responsible and lifelong learners through skill development and certification programmes.

Objectives

- To provide hands-on training in VLSI Design and Semiconductor Technologies using industry-standard EDA tools, FPGA platforms, and semiconductor design workflows.
- To promote industry-academia collaboration, research, consultancy, and innovative semiconductor product development through collaborative initiatives.
- To enhance students' employability and support the Chip to Startup (C2S) Programme through skill development, workshops, hackathons, and expert-led technical training.

Outcomes

- Students gain practical expertise in VLSI Design and Semiconductor Technologies through hands-on training, industry-standard EDA tools, FPGA platforms, workshops, and certification programmes.
- Students and faculty enhance innovation and research capabilities through semiconductor projects, product development, publications, and collaborative research initiatives.
- Improved industry readiness and employability through industry interactions, skill development activities, expert sessions and exposure to emerging semiconductor technologies.

CENTRE OF EXCELLENCE VLSI DESIGN CENTRE

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EDA TOOLS

cadence
 Design Systems

synopsys
 Silicon to Software™

Ansys
 Simulation Driven Innovation

COMPCARTA
 High Performance Computing

KEYSIGHT
 TECHNOLOGIES
 Measurement Solutions

SILVACO
 TCAD & Process Simulation



SUPPORTING SUSTAINABLE DEVELOPMENT GOALS

4 QUALITY EDUCATION



Ensure inclusive and equitable quality education and promote lifelong learning opportunities for all.

8 DECENT WORK AND ECONOMIC GROWTH



Promote sustained, inclusive and sustainable economic growth, full and productive employment and decent work for all.

9 INDUSTRY, INNOVATION AND INFRASTRUCTURE



Build resilient infrastructure, promote inclusive and sustainable industrialization and foster innovation.



SKILL DEVELOPMENT



INDUSTRY COLLABORATION



STARTUP SUPPORT



INNOVATION & RESEARCH



PLACEMENT OPPORTUNITIES



GLOBAL COMPETENCE

*Design • Innovate • Build
 the Future*